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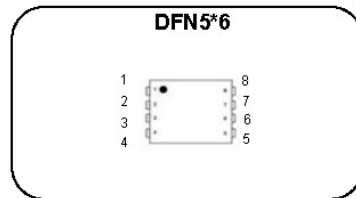
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N-channel Enhanced mode DFN5*6 MOSFET

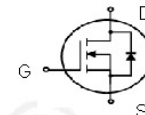
Features

- High ruggedness
- Low $R_{DS(ON)}$ (Typ 12.4mΩ)@ $V_{GS}=4.5V$
(Typ 11.6mΩ)@ $V_{GS}=10V$
- Low Gate Charge (Typ 117nC)
- Improved dv/dt Capability
- 100% Avalanche Tested
- Application: Synchronous Rectification, Li Battery Protect Board, Inverter



4.Gate 5,6,7,8.Drain 1,2,3.Source

BV_{DSS} : 100V
 I_D : 35A
 $R_{DS(ON)}$: 12.4mΩ@ $V_{GS}=4.5V$
 11.6mΩ@ $V_{GS}=10V$



General Description

This power MOSFET is produced with advanced technology of SAMWIN. This technology enable the power MOSFET to have better characteristics, including fast switching time, low on resistance, low gate charge and especially excellent avalanche characteristics.



Order Codes

Item	Sales Type	Marking	Package	Packaging
1	SW HA 35N10V	SW35N10V	DFN5*6	REEL

Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{DSS}	Drain to source voltage	100	V
I_D	Continuous drain current (@ $T_a=25^{\circ}C$)	35*	A
	Continuous drain current (@ $T_a=70^{\circ}C$)	7*	A
I_{DM}	Drain current pulsed (note 1)	140	A
V_{GS}	Gate to source voltage	±20	V
E_{AS}	Single pulsed avalanche energy (note 2)	392	mJ
E_{AR}	Repetitive avalanche energy (note 1)	22	mJ
dv/dt	Peak diode recovery dv/dt (note 3)	5	V/ns
P_D	Total power dissipation (@ $T_a=25^{\circ}C$)	2.7	W
	Derating factor above 25°C	0.02	W/°C
T_{STG}, T_J	Operating junction temperature & storage temperature	-55 ~ + 150	°C

*. Drain current is limited by junction temperature.

Thermal characteristics

Symbol	Parameter	Value	Unit
R_{thja}	Thermal resistance, Junction to ambient	47	°C/W

Note: R_{thja} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{thjc} is guaranteed by design while R_{thca} is determined by the user's board design.



DFN5*6 R_{thja} : 47°C/W on a 1 in² pad of 2oz copper.

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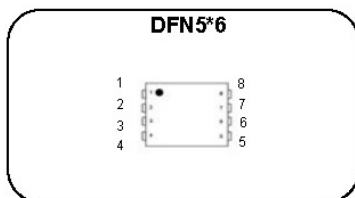
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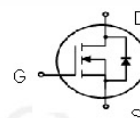
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